

# UMP LAB PROFILE



FTEK

ET-L-09

Digital  
Electronics  
Lab

Version

28th September 2016

# BASIC INFORMATION

**Location:**  
FTEK Lab, Bangunan TTI

**Program;**  
BTE

No	Item	Status
1	Area	51' x 20 '
2	Supply	1 phase
3	Communication point	17 nos network, 1 nos phone
4	Gases	NIL
5	AV System	1 nos Lecturer PC 1 nos LCD Projector 1 nos Projector Screen (Motorized) 1 set PA System
6	Store	NIL
7	Bilik Persediaan Makmal	1 PLV
8	Air Conditioning	2 nos Split Unit
9	Others (utilities)	

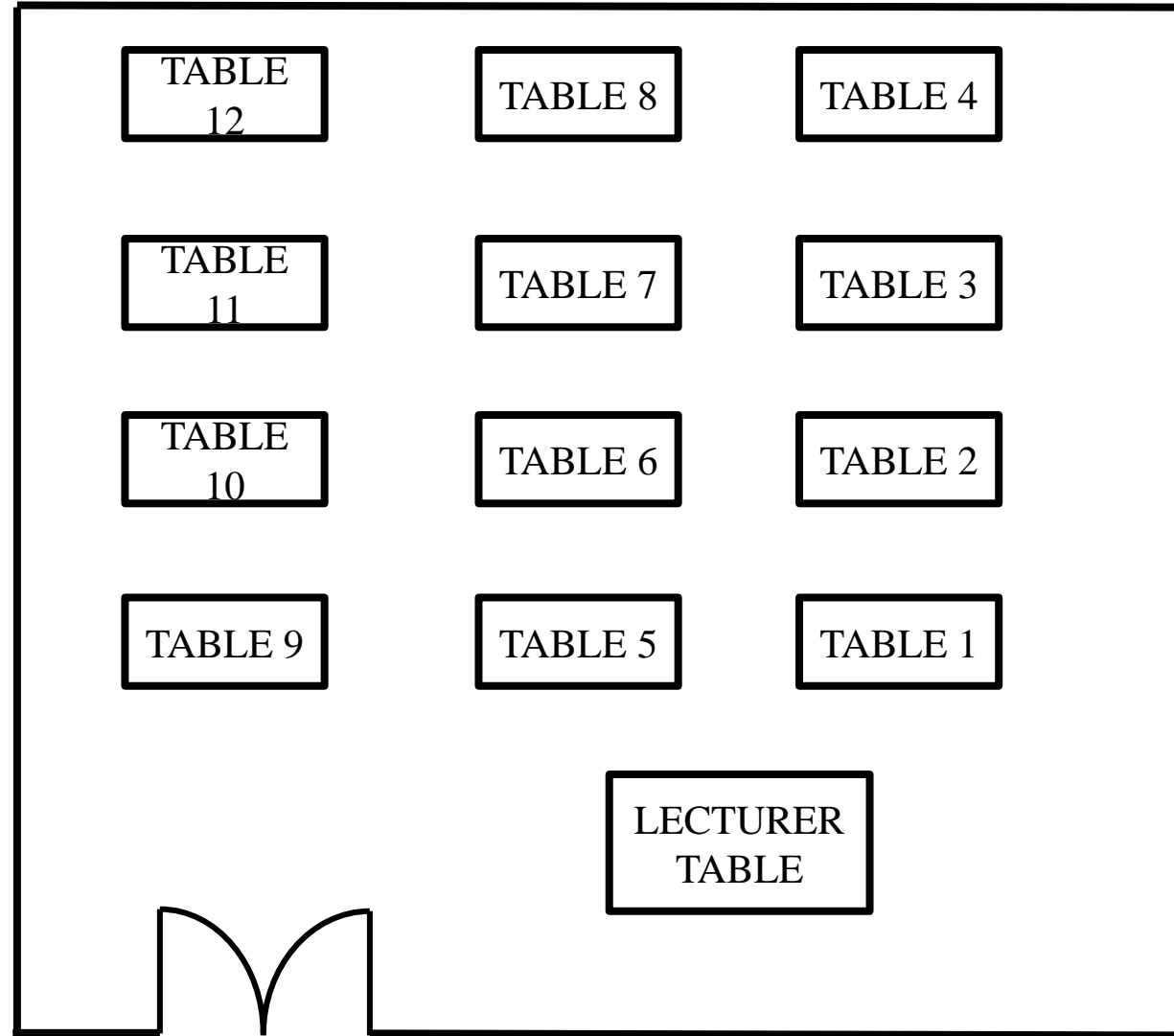
# LIST OF EQUIPMENTS

## Nature of Equipments

- This lab consist of Digital Electronics and Microprocessor System equipment's.
- Experiments are related to Digital Logic Design Lab and Microprocessor and Interfacing lab.

No	Item	Quantity	Status	usage
1	Digital Trainer	17	ok	PnP
2	DC Power Supply	30	ok	PnP
3	Oscilloscope	15	ok	PnP
4	Function Generator	15	ok	PnP
5	Computer Desktop	15	ok	PnP
6	Multimeter	15	ok	PnP
7	Atmega32 Development System	17	ok	PnP

# LAB LAYOUT



# LAB PICTURES



# OTHER INFORMATION

- This lab is using for subject Digital Logic design Laboratory , Microprocessor and Interfacing Laboratory , Electronic II and Fundamental EE Lab.